

**UTILITY PATENT**  
**APPLICATION TRANSMITTAL LETTER**

**067**      **00940000**

Enclosed for filing is the utility patent application of Jean-Marc SARAT for “Versatile Interface Smart Card”.

[X] 4 sheet(s) of [X] formal [ ] informal drawing(s);

[ ] a certified copy of the priority document;

[ ] a Constructive Petition for Extensions of Time;

[ ] \_\_\_\_\_ statement(s) claiming small entity status;

[X] an Assignment document;

[ ] an Information Disclosure Statement; and

[ ] Other: \_\_\_\_\_

The declaration of the inventor(s) ☒ also is enclosed ☐ will follow.

(01/98)

The filing fee has been calculated as follows [ ] and in accordance with the enclosed preliminary amendment:

CLAIMS					
	NO. OF CLAIMS		EXTRA CLAIMS	RATE	FEE
Basic Application Fee					\$790.00
Total Claims	25	MINUS 20 =	5	x \$22.00	110.00
Independent Claims	4	MINUS 3 =	1	x \$82.00	82.00
If multiple dependent claims are presented, add \$270.00					-0-
Total Application Fee					982.00
If verified Statement claiming small entity status is enclosed, subtract 50% of Total Application Fee					-0-
Add Assignment Recording Fee of \$40.00 if Assignment document is enclosed					40.00
<b>TOTAL APPLICATION FEE DUE</b>					<b>1,022.00</b>

[X] A check in the amount of \$ 1,022.00 is enclosed for the fee due.

[ ] Charge \$ \_\_\_\_\_ to Deposit Account No. 02-4800 for the fee due.

Please address all correspondence concerning the present application to:

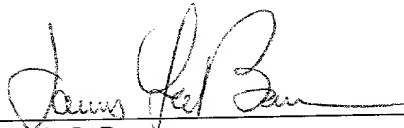
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The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in triplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: March 26, 1998

By:   
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**COMBINED DECLARATION AND POWER OF ATTORNEY  
FOR UTILITY PATENT APPLICATION**

Attorney's Docket No.  
032326-003

As a below-named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I BELIEVE I AM THE ORIGINAL, FIRST AND SOLE INVENTOR (if only one name is listed below) OR AN ORIGINAL, FIRST AND JOINT INVENTOR (if more than one name is listed below) OF THE SUBJECT MATTER WHICH IS CLAIMED AND FOR WHICH A PATENT IS SOUGHT ON THE INVENTION ENTITLED:

"Versatile Interface Smart Card"

the specification of which

(check one)

☒ is attached hereto;

☐ was filed on \_\_\_\_\_ as

Application No. \_\_\_\_\_

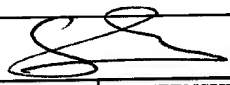
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I HAVE REVIEWED AND UNDERSTAND THE CONTENTS OF THE ABOVE-IDENTIFIED SPECIFICATION, INCLUDING THE CLAIMS, AS AMENDED BY ANY AMENDMENT REFERRED TO ABOVE;

I ACKNOWLEDGE THE DUTY TO DISCLOSE TO THE OFFICE ALL INFORMATION KNOWN TO ME TO BE MATERIAL TO PATENTABILITY AS DEFINED IN TITLE 37, CODE OF FEDERAL REGULATIONS, Sec. 1.56 (as amended effective March 16, 1992);

I do not know and do not believe the said invention was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to said application; that said invention was not in public use or on sale in the United States of America more than one year prior to said application; that said invention has not been patented or made the subject of an inventor's certificate issued before the date of said application in any country foreign to the United States of America on any application filed by me or my legal representatives or assigns more than twelve months prior to said application;

I hereby claim foreign priority benefits under Title 35, United States Code Sec. 119 and/or Sec. 365 of any foreign application(s) for patent or inventor's certificate as indicated below and have also identified below any foreign application for patent or inventor's certificate on this invention having a filing date before that of the application(s) on which priority is claimed:

COMBINED DECLARATION AND POWER OF ATTORNEY			Attorney's Docket No. 032326-003																																																																															
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<p>I hereby appoint the following attorneys and agent(s) to prosecute said application and to transact all business in the Patent and Trademark Office connected therewith and to file, prosecute and to transact all business in connection with international applications directed to said invention:</p> <table style="width: 100%; border: none;"> <tr> <td style="width: 33%;">William L. Mathis</td> <td style="width: 10%;">17,337</td> <td style="width: 33%;">Robert G. Mukai</td> <td style="width: 10%;">28,531</td> <td style="width: 33%;">Bruce J. Boggs, Jr.</td> <td style="width: 10%;">32,344</td> </tr> <tr> <td>Peter H. Smolka</td> <td>15,913</td> <td>George A. Hovanec, Jr.</td> <td>28,223</td> <td>William H. Benz</td> <td>25,952</td> </tr> <tr> <td>Robert S. Swecker</td> <td>19,885</td> <td>James A. LaBarre</td> <td>28,632</td> <td>Peter K. Skiff</td> <td>31,917</td> </tr> <tr> <td>Platon N. Mandros</td> <td>22,124</td> <td>E. Joseph Gess</td> <td>28,510</td> <td>Richard J. McGrath</td> <td>29,195</td> </tr> <tr> <td>Benton S. Duffett, Jr.</td> <td>22,030</td> <td>R. Danny Huntington</td> <td>27,903</td> <td>Matthew L. Schneider</td> <td>32,814</td> </tr> <tr> <td>Joseph R. Magnone</td> <td>24,239</td> <td>Eric H. Weisblatt</td> <td>30,505</td> <td>Michael G. Savage</td> <td>32,596</td> </tr> <tr> <td>Norman H. Stepno</td> <td>22,716</td> <td>James W. Peterson</td> <td>26,057</td> <td>Gerald F. Swiss</td> <td>30,113</td> </tr> <tr> <td>Ronald L. Grudziecki</td> <td>24,970</td> <td>Teresa Stanek Rea</td> <td>30,427</td> <td>Michael J. Ure</td> <td>33,089</td> </tr> <tr> <td>Frederick G. Michaud, Jr.</td> <td>26,003</td> <td>Robert E. Krebs</td> <td>25,885</td> <td>Charles F. Wieland III</td> <td>33,096</td> </tr> <tr> <td>Alan E. Kopecki</td> <td>25,813</td> <td>Robert M. Schulman</td> <td>31,196</td> <td>Bruce T. Wieder</td> <td>33,815</td> </tr> <tr> <td>Regis E. Slutter</td> <td>26,999</td> <td>William C. Rowland</td> <td>30,888</td> <td>Todd R. Walters</td> <td>34,040</td> </tr> <tr> <td>Samuel C. Miller, III</td> <td>27,360</td> <td>T. Gene Dillahunt</td> <td>25,423</td> <td> </td> <td> </td> </tr> <tr> <td>Ralph L. Freeland, Jr.</td> <td>16,110</td> <td>Patrick C. Keane</td> <td>32,858</td> <td> </td> <td> </td> </tr> </table>					William L. Mathis	17,337	Robert G. Mukai	28,531	Bruce J. Boggs, Jr.	32,344	Peter H. Smolka	15,913	George A. Hovanec, Jr.	28,223	William H. Benz	25,952	Robert S. Swecker	19,885	James A. LaBarre	28,632	Peter K. Skiff	31,917	Platon N. Mandros	22,124	E. Joseph Gess	28,510	Richard J. McGrath	29,195	Benton S. Duffett, Jr.	22,030	R. Danny Huntington	27,903	Matthew L. Schneider	32,814	Joseph R. Magnone	24,239	Eric H. Weisblatt	30,505	Michael G. Savage	32,596	Norman H. Stepno	22,716	James W. Peterson	26,057	Gerald F. Swiss	30,113	Ronald L. Grudziecki	24,970	Teresa Stanek Rea	30,427	Michael J. Ure	33,089	Frederick G. Michaud, Jr.	26,003	Robert E. Krebs	25,885	Charles F. Wieland III	33,096	Alan E. Kopecki	25,813	Robert M. Schulman	31,196	Bruce T. Wieder	33,815	Regis E. Slutter	26,999	William C. Rowland	30,888	Todd R. Walters	34,040	Samuel C. Miller, III	27,360	T. Gene Dillahunt	25,423			Ralph L. Freeland, Jr.	16,110	Patrick C. Keane	32,858		
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<p>and: _____</p> <p>Address all correspondence to: <b>James A. LaBarre</b>  <b>BURNS, DOANE, SWECKER &amp; MATHIS, L.L.P.</b>  <b>P.O. Box 1404</b>  <b>Alexandria, Virginia 22313-1404</b></p> <p>Address all telephone calls to: <u>James A. LaBarre</u> at (703) 836-6620.</p> <p>I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.</p>																																																																																		
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**APPLICATION FOR UNITED STATES  
LETTERS PATENT**

**by**

**JEAN-MARC SARAT**

**for**

**VERSATILE INTERFACE SMART CARD**

**BURNS, DOANE, SWECKER & MATHIS, L.L.P.**  
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**Attorney Docket: 032326-003**

## VERSATILE INTERFACE SMART CARD

### Field of the Invention

5 The present invention is directed to microprocessor-based user cards, commonly known as "smart cards", and more particularly to a smart card that is capable of communicating with a variety of external devices using different protocols that are respectively associated with the various devices.

### Background of the Invention

10 The use of secure smart cards that provide information specific to an individual is becoming more prevalent in a number of different types of situations. Examples of such include electronic commerce, security access control and health care record maintenance. Each system which employs smart cards contains two fundamental components, namely the smart cards themselves and an interface device, commonly known as a reader. The smart cards are carried by the users of the system, and include a memory which stores information that is pertinent to the user's interaction with the system. In an electronic commerce system, for 15 example, each smart card may contain the balance in an account maintained by the user, as well as details of account transactions. More recently, the smart cards also include microprocessors, which provide for an increased level of security over the information stored in the cards. The incorporation of microprocessors into the cards also enhances their flexibility, for instance by facilitating the storage of executable programs in the cards that can be used to provide expanded 20 functionality.

25 The readers communicate with the cards in a secure manner to access the information stored therein. In one type of system, the card is inserted into a slot in the reader, which brings electrical contacts in the reader into engagement with mating contacts on the exterior of the card. The engaged contacts enable a microcontroller in the reader to communicate with the memory and/or microprocessor in the card. Typically, the reader is connected to a peripheral

device that is associated with the particular type of system into which the reader is incorporated. In a security system, for example, the reader might be connected to an electronic lock that permits a door to be opened. In a banking system, the reader could be incorporated into an automatic teller machine.

5           To permit the cards and readers of different manufacturers to be compatible with one another, a set of standard specifications has been developed. One of the common standards that applies to smart cards and readers is ISO 7816, promulgated by the International Standards Organization. This standard provides specifications for the location of the electrical contacts on the exterior of the cards,  
10 as well as the functions of the electrical signals that are present at the respective contacts. In this regard, the standard provides for up to eight electrical contacts, although specific signals are defined for only five of these contacts. The standard also contains specifications for the power-up, or initialization, procedure that is carried out when a card is first inserted into the reader, and the protocol for  
15 communicating between the card and the reader.

Due to the need to comply with the published standards, a conventional card reader can turn out to be a relatively expensive item of equipment. For instance, the ISO standard requires that different respective signals be applied to the five designated contacts on the card in a specific sequence at predetermined  
20 times during the power-up procedure. As a result, the reader must include a controller which supervises the application and timing of these signals, thereby adding to its cost.

In many systems which currently employ smart cards, the number of users can be quite large. For example, in an electronic banking system, a considerable  
25 number of customers might be expected to access an automated teller machine each day. Consequently, the cost of the reader is amortized over a sufficient number of transactions that it can be readily justified by the provider of the services.

More recently, there has been a trend toward personalized types of smart card applications. For instance, the ability to execute software programs from a

microprocessor-based smart card makes it desirable to be able to connect the card to a personal computer. One approach for doing this is to add a smart card reader to the computer, either as an integral device or as a peripheral add-on. However, due to the appreciable cost of a reader, personal computer users may not be inclined to adopt this approach. In contrast to large institutions such as banks and the like, individual computer users may not be able to amortize the cost of the reader over a sufficient number of transactions to justify its cost.

It is desirable, therefore, to provide a smart card system which does not require a relatively expensive reader to access the information and/or functionality present in a card. With such a capability, the smart card is able to directly communicate with a variety of different types of peripheral devices that do not require a protocol associated with ISO standards and the like. Consistent with this objective, however, it is further desirable to provide such a system which remains compatible with currently existing smart card systems that comply with established standards.

### **Summary of the Invention**

In accordance with the present invention, these objectives are achieved by providing a smart card that is compatible with multiple different protocols. In one embodiment, such a card is fully compatible with the protocols of the ISO standard and another, non-ISO, standard. One of the contacts of the card which is not designated by the standard is used to indicate whether the card is to operate in the ISO-standard mode, or in another mode. When no signal is present at this terminal, the card operates in the conventional ISO-standard mode. However, the presence of a predetermined signal provides an indication that the card is to operate in the non-ISO mode. In this case, a different start-up procedure can be employed which does not require the strict timing constraints associated with the ISO mode of operation. Consequently, a less expensive reader can be employed



which may not require the type of controller that is associated with an ISO-compliant reader.

In a specific embodiment, the non-ISO mode can be one that is associated with standard communication protocols that are used in personal computers, such as PS/2, USB and the like. An interface device which communicates with a multi-protocol smart card can be connected to any bus of the computer which operates in accordance with the desired non-ISO protocol. In the case of PS/2, for example, the interface device can be connected between the keyboard and the CPU of the computer system. Whenever a smart card is inserted into such an interface device, it communicates with both the CPU and the keyboard in accordance with the PS/2 protocol.

The applications of the multi-protocol card are not limited to communications with personal computers. Due to the flexibility and functionality offered by smart cards that have microprocessors incorporated therein, the multi-protocol smart card can be used to drive, or otherwise communicate with, any of a variety of peripheral devices, whether or not a personal computer is present in the system.

Further features of the invention, and the advantages offered thereby, are explained in detail hereinafter, with reference to specific embodiments of the invention illustrated in the accompanying drawings.

#### **Brief Description of the Drawings**

Figure 1 is a perspective view of a personal computer system which is configured to operate with a smart card;

Figure 2 is a plan view of a smart card and a reader;

Figure 3A is a more detailed view of the electronic components of a conventional smart card;

Figure 3B is a detailed view of the electronic components of a smart card in accordance with a first embodiment of the present invention;

Figure 4 is a flowchart of the general mode of operation of the present invention;

Figure 5A is a block diagram of a reader that conforms to the ISO standard;

5        Figure 5B is a schematic diagram of an interface device in accordance with the present invention;

Figure 6 is a block diagram of a second embodiment of the present invention; and

Figure 7 is a block diagram of a third embodiment of the present invention.

10        **Detailed Description**

To facilitate an understanding of the principles and features of the present invention, it is described hereinafter in the context of a specific embodiment. In particular, reference is made to an implementation of the invention in which a smart card can be connected to a personal computer. It will be appreciated,  
15        however, that the practical applications of the invention are not limited to this particular embodiment. Rather, the invention can be employed in a variety of different systems in which it is desirable to utilize the functionality of a smart card without the need for a reader that strictly conforms to published standards for smart cards.

20        Figure 1 is an illustration of a conventional personal computer system which is configured to operate with a smart card. As is typical, the computer system might include a central processing unit (CPU) 10 and the basic input and output devices that are employed by the user to interact with programs being executed by the CPU, such as a keyboard 12 and a monitor 14. In addition, the  
25        system shown in Figure 1 includes a smart card reader 16 as another peripheral device. In a conventional arrangement, the reader conforms to the ISO standard, and the exchange of information between a smart card and the reader is carried out in accordance with a protocol established by that standard. The reader can be

connected to the CPU via a standard input/output port, such as an RS232 serial port. Alternatively, the structure of the reader might be incorporated into the housing for the CPU or the keyboard, which would then include an appropriate slot for insertion of the smart card.

5           A configuration such as that shown in Figure 1 might be desirable for a variety of different applications. At a first level, the smart card can be used to control access to the personal computer. For instance, before the user is allowed to run a particular program or open a sensitive file, he or she may be required to insert his or her personal smart card into the reader 16 and enter a password via  
10   the keyboard 12. In a known manner, the smart card and the reader communicate with one another to authenticate the password, and thereafter authorize the computer to operate in the manner commanded by the user. In a more sophisticated application, the smart card might include one or more personalized software programs which can be executed by the microprocessor in the smart card  
15   and interact with the CPU, such as an electronic banking program.

          One of the concerns with a configuration such as that shown in Figure 1 is the fact that the security of information pertaining to the smart card could be compromised. In particular, when the user enters his or her password via the keyboard, that information must pass through the control of the CPU in order to be  
20   presented to the smart card. This communication path is not entirely secure, since it is capable of being externally accessed. For instance, the CPU could be programmed to capture and store the user's password as it is being transmitted to the reader. The stored password could then be retrieved at a later time by a person who is not the rightful owner of the smart card.

25           As described previously, another concern is the fact that the reader itself can be relatively expensive, particularly if it is ISO-compliant. Because of the expense, many individual users may not desire to purchase such a device, and thereby forego the added functionality that a smart card would otherwise provide.

In accordance with the present invention, these concerns are alleviated by providing a smart card that is capable of operating in a mode other than that which is defined by the ISO standard. For instance, a smart card which can operate in accordance with the PS/2 protocol would be capable of communicating directly  
5 with the keyboard 12, without the intervention of the CPU, and thereby provide a more secure path for the transfer of personalized data, such as a password. In addition, when operating in such a mode, an ISO-compliant reader would not be required, thereby reducing the cost required to configure the computer to work with a smart card.

10 In a preferred embodiment of the invention, the smart card is capable of selectively operating in both a normal ISO-compliant mode, and at least one non-ISO mode, to optimize its utility. In this manner, the smart card can be used with established systems that are commonly associated with ISO readers, such as building access control, electronic banking transactions, and the like. In addition,  
15 it can also be used for other, less frequently employed features, such as to gain access to a protected personal computer.

The basic structure of a smart card system, insofar as it pertains to the present invention, is illustrated in Figure 2. Generally speaking, a smart card 18  
20 is a user card made of plastic or other suitable material, similar to a common credit card, and having a number of electrical contacts 20 on one exterior surface thereof. Embedded within the structure of the card is an electronic memory 22 and, in a preferred embodiment of the invention, a microprocessor 24. For ease of illustration, in Figure 2 the memory 22 and microprocessor 24 are shown offset from the contacts 20, but in practice they can be located directly beneath the  
25 contacts. The dimensions of the card 18, and the arrangement and location of the contacts 20, are determined by applicable standards. The particular card shown in Figure 2 has eight such contacts, although cards with only six contacts also comply with known standards.

The reader 16 has a slot 26 that is appropriately dimensioned to receive the card 18. The bottom of the slot has a switch 28, or other form of sensor, to detect when the card is fully inserted into the slot. The interior surface of the slot has a set of mating contacts (not shown), which engage corresponding contacts 20 on the card when it is fully inserted. When the sensor detects that a card is completely inserted into the slot 26 of the reader, it sends a signal which causes the reader to initiate a power-up procedure, described in detail hereinafter.

An expanded view of the contacts 20 on the smart card, and their connection to the internal microprocessor 24 of the card, is illustrated in Figure 3A. Referring thereto, the ISO standard specifies the particular type of signal that is associated with five of the eight contacts. These signals include power (Vcc), ground, reset, clock and input/output. The other three contacts are not assigned to any signal, and therefore are not used under the ISO protocol. Each of the contacts 20 is connected to the microprocessor 24 by means of a pull-up resistor 30, so that, in the absence of a signal at a given contact, the input signal to the microprocessor 24 is at a logical high level. Hence, the signals at the three unassigned contacts always remain at the logic high level.

In operation, when a card 18 is inserted into the slot 26 of the reader 16, it actuates the sensor 28 when it reaches the end of travel in the direction of the arrow shown in Figure 2. Upon receipt of a signal from the sensor, a microcontroller (not shown) within the reader applies the appropriate signals to the contacts 20 of the card, in a predetermined order specified by the ISO standard. Specifically, the standard requires that the ground reference potential be connected first, followed by the Vcc power supply voltage. Once the power is connected in this sequence, the clock signal is provided, and then a reset signal is applied. Thereafter, communications between the reader and the internal components 22, 24 of the card 18 are carried out via the I/O contact.

In accordance with the present invention, one or more of the unassigned contacts is used to expand the functionality of the smart card, by enabling it to

operate in accordance with non-ISO protocols. Figure 3B illustrates a first embodiment of the contact assignment for a smart card having multi-protocol capabilities. In this embodiment, a sixth contact 32 functions as a mode indicator. When the card is inserted into an ISO-compliant reader, no signal is presented to the mode contact. Consequently, the mode input signal to the card's microprocessor 24 is at a logic high level, as in the normal case. The microprocessor therefore operates in accordance with the standard ISO protocol.

The functionality of the mode contact also permits the card to be operated in accordance with a different type of protocol. In this context, the card is used with an interface device that applies a "non-ISO" mode signal to the mode contact 32. In a card of the type shown in Figure 3A, where each of the inputs to the internal microprocessor 24 is normally pulled to a logic high level, the non-ISO mode signal would therefore be a logic low signal. As depicted in Figure 3B, the interface device might connect the mode contact 32 directly to the ground reference potential that is also supplied to the GND contact. Upon detecting a low-level signal at the mode contact 32, the microprocessor 24 switches its mode of operation, to communicate in accordance with a predefined protocol other than the standard ISO protocol.

Figure 4 illustrates a flow chart which depicts the operation of a smart card in accordance with the first embodiment of the invention. Referring thereto, when the card is fully inserted into the interface device, the microprocessor 24 is first powered up, and a reset signal is then applied from the interface device. In response to the reset signal, the microprocessor performs a standard initialization routine, and then checks the status of the input signal at the mode contact. If the mode signal is at a logic high level, this indicates that the card has been inserted in an ISO-compliant reader, and consequently the microprocessor 24 communicates via the I/O channel using the standard ISO protocol. If, however, the logic signal at the mode contact has been pulled low, the card's microprocessor communicates

via the I/O channel using a non-ISO protocol, such as PS/2, USB or I2C, for example.

One advantage that arises from the ability to operate in a non-ISO mode is the fact the that interface device can be significantly simplified. Figure 5A

5 illustrates a reader that conforms to the ISO standard. That standard requires that each of the respective signals be applied to the five designated contacts of the card in a predetermined order, and with strictly controlled timing, during the power-up procedure. To provide the necessary control over the application of the signals, therefore, the reader 16 includes a microcontroller 34 which receives, as input  
10 signals, the Vcc and ground power signals, as well as a clock signal that is derived from a crystal oscillator 36, or the like. Depending upon the particular application, the microcontroller might receive data signals that are provided from an external source. The microcontroller 34 also receives a signal from the sensor 28, which indicates when the card 18 is fully inserted into the slot 26 in the reader.  
15 In response to this signal, the microcontroller controls the application of the respective signals to each of the five assigned contacts of the card.

In a non-ISO mode of operation, the timing requirements associated with the power-up procedure are not as rigid, and therefore a more conventional initialization circuit can be employed. For instance, the reset signal can be  
20 generated by a simple RC timing circuit, as illustrated in Figure 5B. Upon receipt of a signal from the sensor 28, the RC timing circuit is actuated to apply a reset pulse to the appropriate contact 20 after a period of time that is determined by its timing constant. Hence, the need for a more expensive microcontroller can be eliminated, thereby reducing the cost of the interface device. Of course, any other  
25 suitable type of reset circuit can be employed as well. To distinguish this simplified interface device from a conventional ISO-compliant reader, it will be referred to herein as an adapter.

Another advantage that stems from the ability to operate in accordance with different protocols lies in the fact that the card can communicate directly with

devices that employ non-ISO protocols. This aspect of the invention is described in greater detail with reference to a second embodiment, which is schematically illustrated in Figure 6. For ease of illustration, the layout of the contacts is rearranged in Figure 6, relative to that shown in Figures 3 and 5. In this particular embodiment, the adapter, and hence the card, is located in the communication path between the CPU 10 and the keyboard 12 of a personal computer system. In a computer which utilizes the Micro Channel Architecture, for example, communications over the bus between the keyboard and the CPU are carried out in accordance with the PS/2 protocol. Figure 6 illustrates the four basic signal lines that are present in this bus. These signal lines include the two power lines associated with the ground reference potential and a positive voltage, e.g. 5 volts. The other two lines constitute an input/output path for data signals, and a path for the data rate clock.

The adapter for the smart card is connected to the keyboard bus in the manner illustrated in Figure 6. The two power lines are directly connected to the corresponding contacts of the adapter. In addition, the clock line of the keyboard bus is connected to one of the three terminals that are not designated according to the ISO standard. It is to be noted that the clock signal which appears on the keyboard bus is different from the clock signal that is applied to the internal microprocessor 24 of the card. Specifically, the clock which is applied to the microprocessor is one which controls the overall operation associated with the microprocessor, and is labeled  $CLK_M$ . This clock signal is applied to the usual clock terminal, by means of an oscillator 36 within the adapter, for example. In contrast, the clock signal  $CLK_D$  which is obtained from the keyboard bus pertains to the rate at which data is transmitted over the I/O channel of the bus. Since this clock signal is not defined in the ISO standard, it is applied to one of the unassigned contact terminals.

A switch 38 is inserted in the I/O channel of the keyboard bus, to permit this channel to be opened whenever a card is inserted in the adapter. To this end,



the switch is responsive to the sensor 28 which detects that the card is fully inserted into the adapter. In an exemplary embodiment, the switch can be mechanically opened by the card itself. The I/O channel on one side of the switch is connected to the normal I/O terminal of the smart card, which is labelled I/O<sub>1</sub>.

5 Another one of the normally unassigned terminals of the card constitutes a second I/O contact, and is connected to the I/O channel of the bus that is on the other side of the switch. When no card is inserted in the adapter, data is bidirectionally transmitted between the keyboard and the CPU over the bus I/O channel, in a normal fashion, with the switch closed. When a card is inserted into the adapter,  
10 the data passes through the two I/O terminals on the card. The smart card can operate in a passive mode or an active mode. In a passive mode, the data is simply transferred between the two I/O terminals, without disturbance. In an active mode, the microprocessor 24 in the card can receive data transmitted by either the CPU or the keyboard, and likewise can transmit data to either or both of these  
15 devices.

The direct connection between the smart card and each of the CPU and the keyboard provides a secure channel for the transfer of sensitive data. For example, in a situation where the user enters his or her password via the keyboard, that data is transmitted directly to the smart card, without going to the CPU. Once  
20 the proper password has been entered, the functionality provided by the smart card can then be employed in connection with the personal computer. For example, electronic banking transactions can be carried out in connection with account information that is securely stored in the smart card.

From the foregoing, it will be appreciated that the ability to operate the  
25 smart card in accordance with different protocols permits the smart card to be used in connection with a variety of different types of peripheral devices. While the embodiment of Figure 6 comprises a system that includes a personal computer with a keyboard, it is not necessary that an external CPU be present. Rather, the smart card could be used with standalone devices, such as a PIN pad for entering

passwords, a display device, or a modem. The only requirement is that the peripheral device include an I/O channel and a data clock which can be accessed by the smart card, as in the embodiment of Figure 6. As an example, Figure 7 illustrates an embodiment in which the smart card is used to control a terminal which essentially comprises a keyboard and a display. All of the functionality that is needed to operate the terminal is contained within the smart card itself. One I/O contact of the smart card is used for bidirectional communications with the keyboard, and the other I/O contact functions to drive the display. A particular advantage of this arrangement resides in the fact that executable programs, such as applets written in the JAVA programming language, can be downloaded into the smart card, and then used to control the terminal to provide a variety of different functions. Since the JAVA programming language is not platform specific, the terminal need not be uniquely associated with a given card. Rather, terminals from a variety of different manufacturers can be operated with the same card.

This type of operation provides additional security as well. Since all of the operations of the terminal are managed by the card itself, there is no need to provide any form of intelligence within the terminal itself, or to store any secure type of information therein. Furthermore, while the multi-protocol card offers this expanded range of functionality, it still remains compatible with the ISO standard, and therefore can be used for conventional smart card applications.

It will be appreciated by those of ordinary skill in the art that the present invention can be embodied in other specific forms without departing from the spirit or essential characteristics thereof. For example, while the disclosed embodiments depict a card which can selectively operate in accordance with two protocols, it is possible to employ a greater number of protocols for a greater flexibility. For instance, the mode contact 32 can indicate whether the card is to operate pursuant to the ISO protocol or a non-ISO protocol. If a non-ISO protocol is to be used, data provided over one of the I/O channels can identify any one of a plurality of different protocols to be used with the adapter in the non-ISO mode.



**What is claimed is:**

1. A multi-protocol smart card system, comprising:  
a user card containing a microprocessor and associated memory, and a plurality of contacts for transferring data to and from said microprocessor and memory, said contacts including a first set of contacts respectively associated with a set of signals that conform to a first protocol, and at least one other contact for controlling said microprocessor to operate in accordance with a second protocol;  
and  
an interface device for receiving said user card, and having:  
a first set of mating contacts which correspond to the first set of contacts in said user card, to transfer said signals that conform to said first protocol,  
a mode contact that corresponds to said other contact of the user card, and  
a mode signal generator that provides a signal at said mode contact which causes said microprocessor to operate in accordance with said second protocol mode when the user card is received in said interface device.
2. The smart card system of claim 1, wherein said first protocol is an ISO protocol that pertains to smart cards, and said second protocol is a non-ISO protocol.
3. The smart card system of claim 2 wherein said non-ISO protocol is selected from the group comprising PS/2, USB and I2C protocols.
4. The smart card system of claim 1, wherein said microprocessor selectively operates in accordance with a plurality of non-ISO protocols in accordance with a signal provided by said mode signal generator.

5. The smart card system of claim 1, wherein said other contact of the user card is not used when said microprocessor operates in accordance with said first protocol.

6. The smart card system of claim 5, wherein said other contact is normally maintained at a predetermined logic level during operation in accordance with said first protocol, and said mode signal generator switches said other contact to a different logic level when the microprocessor is to operate in accordance with said second mode.

7. The smart card system of claim 6 wherein said different logic level is a ground reference potential.

8. The smart card system of claim 1 wherein said interface device further includes a reset signal generator for applying a reset signal to one of the contacts of the user card.

9. The smart card system of claim 8 wherein said reset signal is applied to one of the contacts of said first set of contacts.

10. The smart card system of claim 8 wherein said reset signal generator comprises an RC timing circuit.

11. In a transaction system of the type in which a user card having a microprocessor communicates with an interface device to perform a transaction, a method for selectively operating said microprocessor in one of a plurality of modes, comprising the steps of:

placing a user card in an operative relationship with an interface device so as to permit signals to be exchanged between the user card and the interface device;

providing signals to the user card from the interface device by means of a predefined set of communication contacts associated with a first operating protocol;

selectively providing a mode signal to the user card from the interface device in accordance with a mode of operation associated with the interface device;

determining within the microprocessor of the user card whether the mode signal is being provided by the interface device; and

operating said microprocessor in accordance with said first operating protocol when said mode signal is not being provided, and operating said microprocessor in accordance with a second operating protocol different from said first protocol when said mode signal is being provided.

12. The method of claim 11 wherein said mode signal is provided to the user card by means of a communication contact other than the contacts of said predefined set of contacts.

13. The method of claim 11, wherein said first protocol is an ISO protocol that pertains to smart cards, and said second protocol is a non-ISO protocol.

14. The method of claim 13, wherein said non-ISO protocol is selected from the group comprising PS/2, USB and I2C protocols.



a mode signal generator that provides a signal at said mode contact which causes a microprocessor in a user card to operate in accordance with a second protocol mode when the user card is received in said interface device.

19. The interface device of claim 18, wherein said first protocol is an ISO protocol that pertains to smart cards, and said second protocol is a non-ISO protocol.

20. The interface device of claim 19, wherein said non-ISO protocol is selected from the group comprising PS/2, USB and I2C protocols.

21. The interface device of claim 18, wherein said mode contact is normally maintained at a predetermined logic level during operation in accordance with said first protocol, and said mode signal generator switches said other contact to a different logic level when the microprocessor is to operate in accordance with said second mode.

22. The interface device of claim 21 wherein said different logic level is a ground reference potential.

23. The interface device of claim 18 wherein said interface device further includes a reset signal generator for applying a reset signal to one of the contacts of the user card.

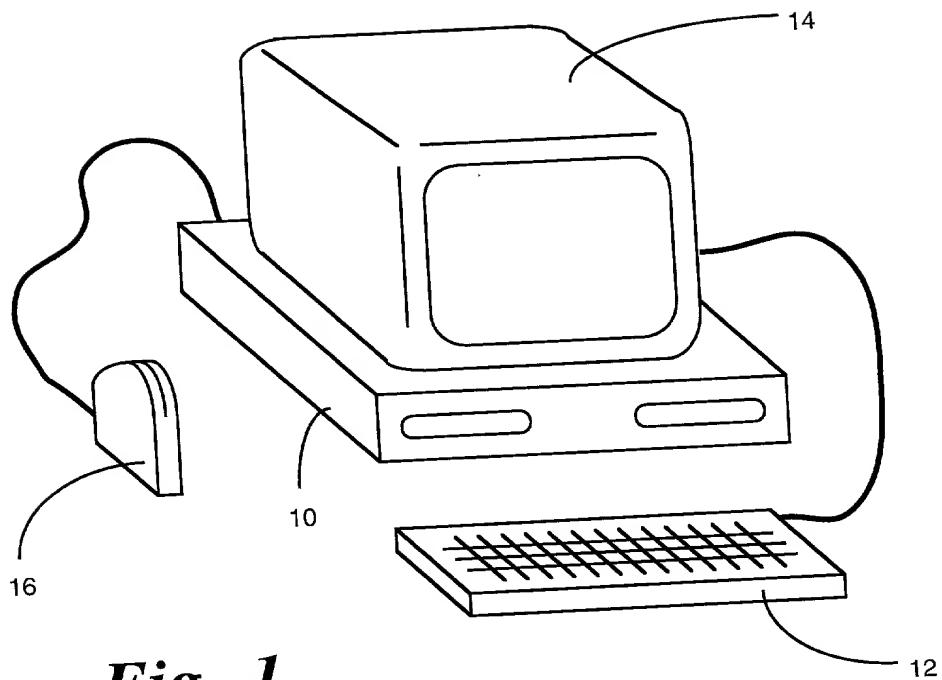
24. The interface device of claim 23 wherein said reset signal is applied to one of the contacts of said first set of contacts.

25. The interface device of claim 23 wherein said reset signal generator comprises an RC timing circuit.

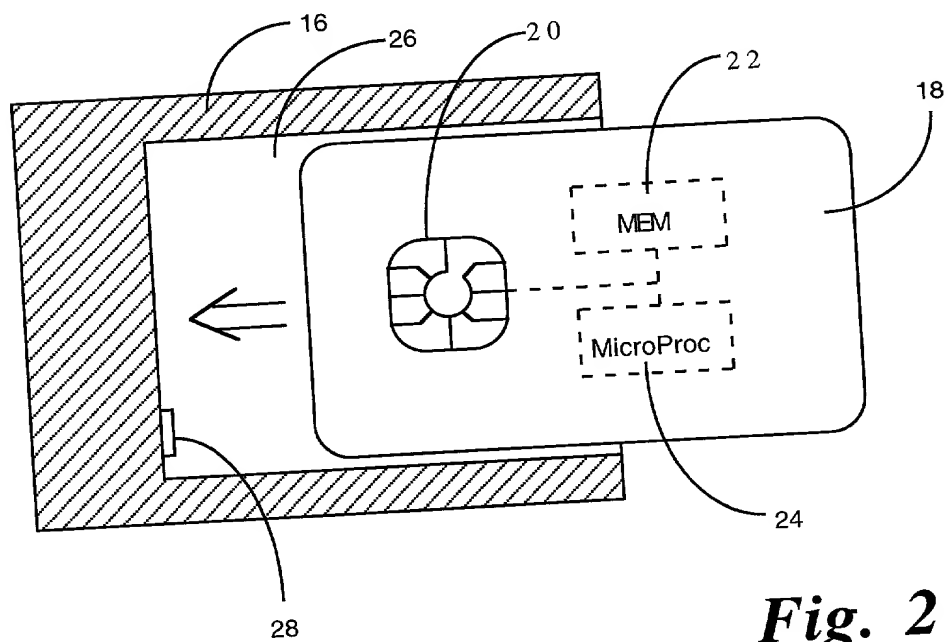


**ABSTRACT OF THE DISCLOSURE**

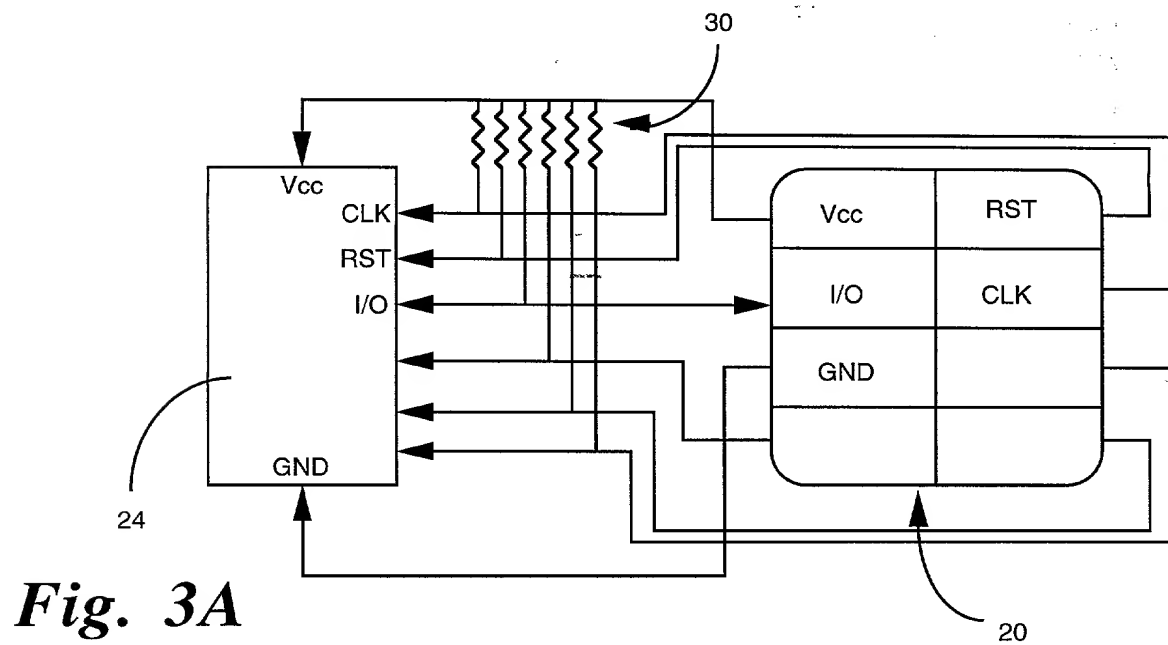
5 A smart card that is compatible with multiple different protocols includes a standard set of contacts that comply with the protocols of a published standard, and another contact not designated by the standard which is used to indicate whether the card is to operate in a non-standard mode. When the card is to operate in the non-standard mode, a simple start-up procedure is employed which does not require strict timing constraints, enabling a less expensive interface device to be used. The interface device can be connected to any bus of a computer which operates in accordance with a desired non-standard protocol. Due to the flexibility and functionality offered by smart cards that have microprocessors incorporated therein, the multi-protocol smart card can be used to drive, or otherwise  
10 communicate with, any of a variety of peripheral devices, whether or not a personal computer is present in the system.



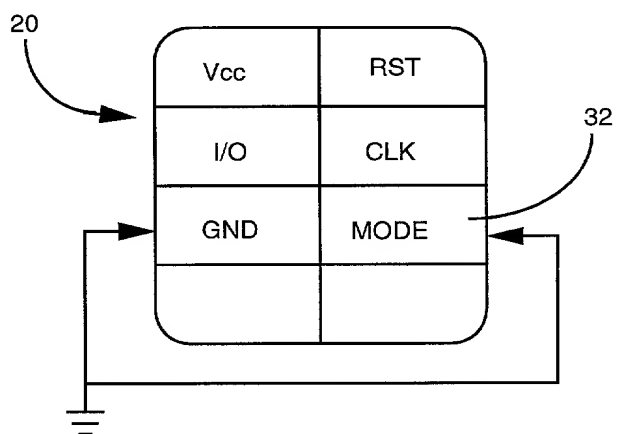
**Fig. 1**



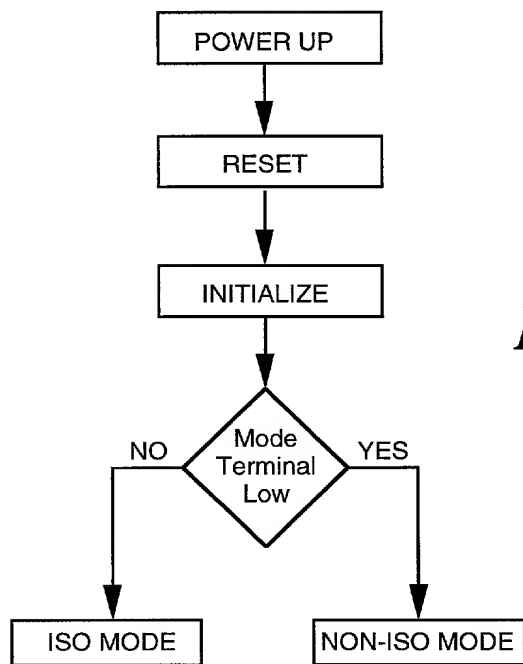
**Fig. 2**



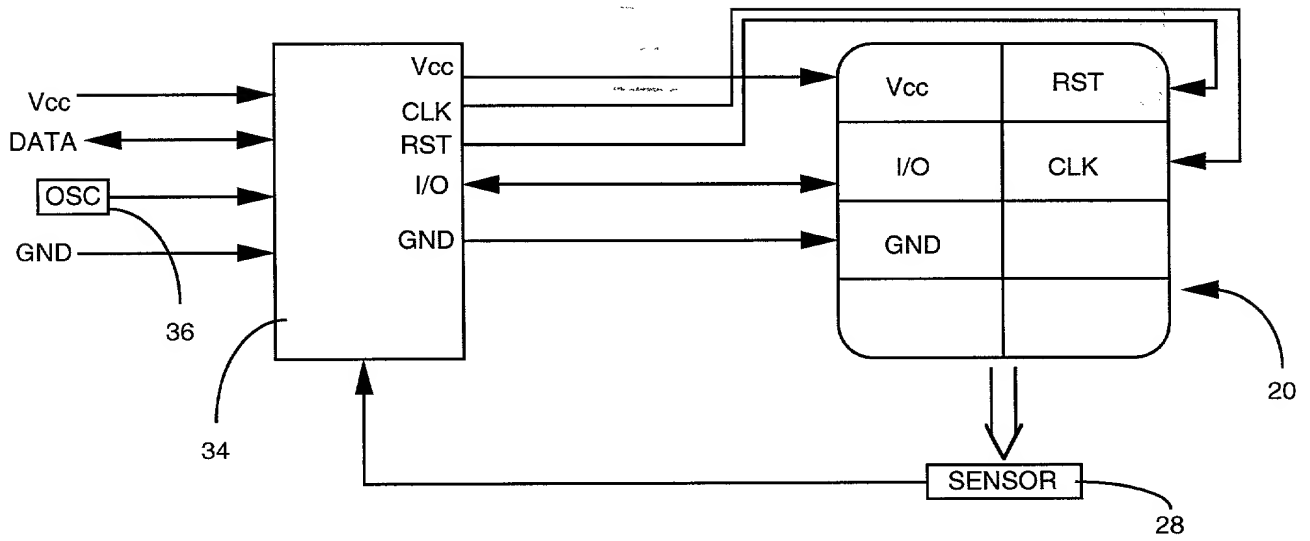
**Fig. 3A**



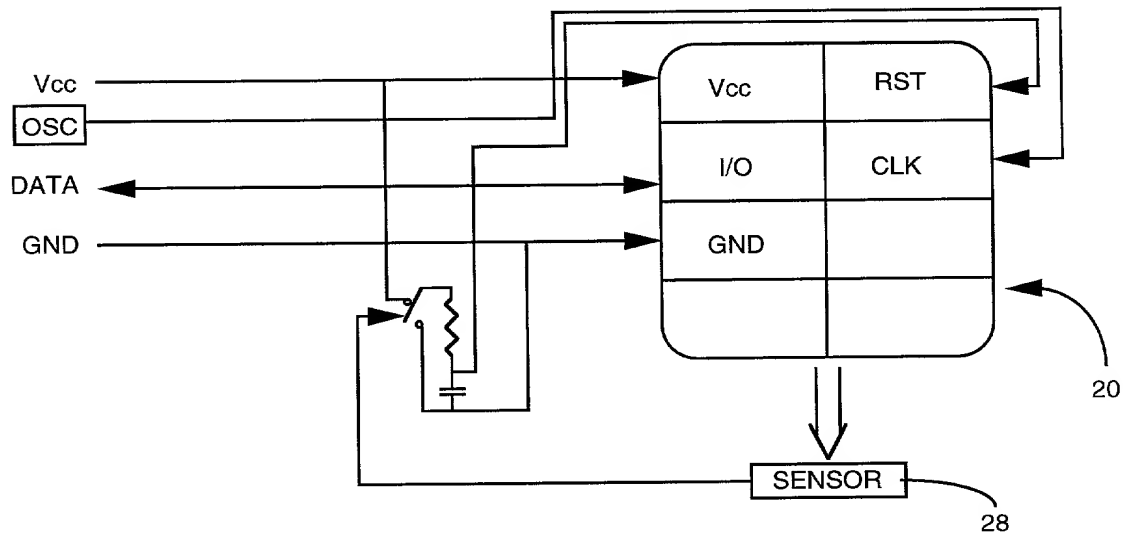
**Fig. 3B**



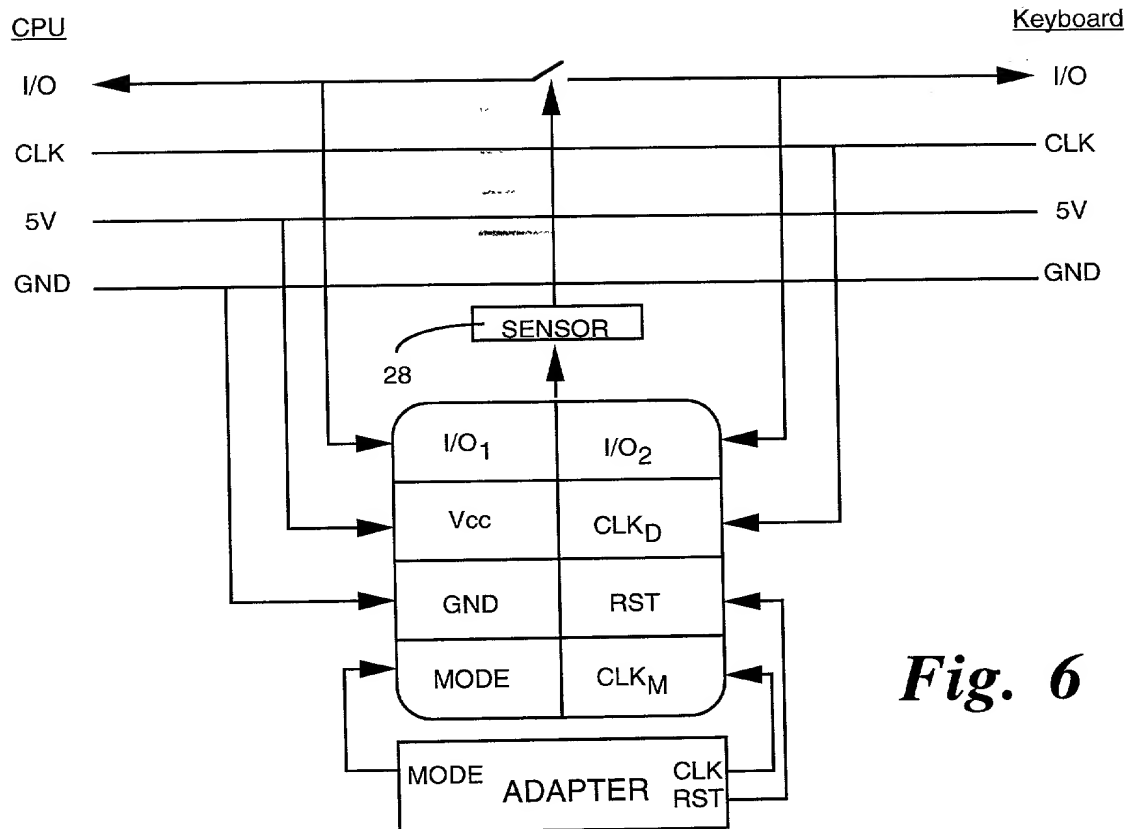
**Fig. 4**



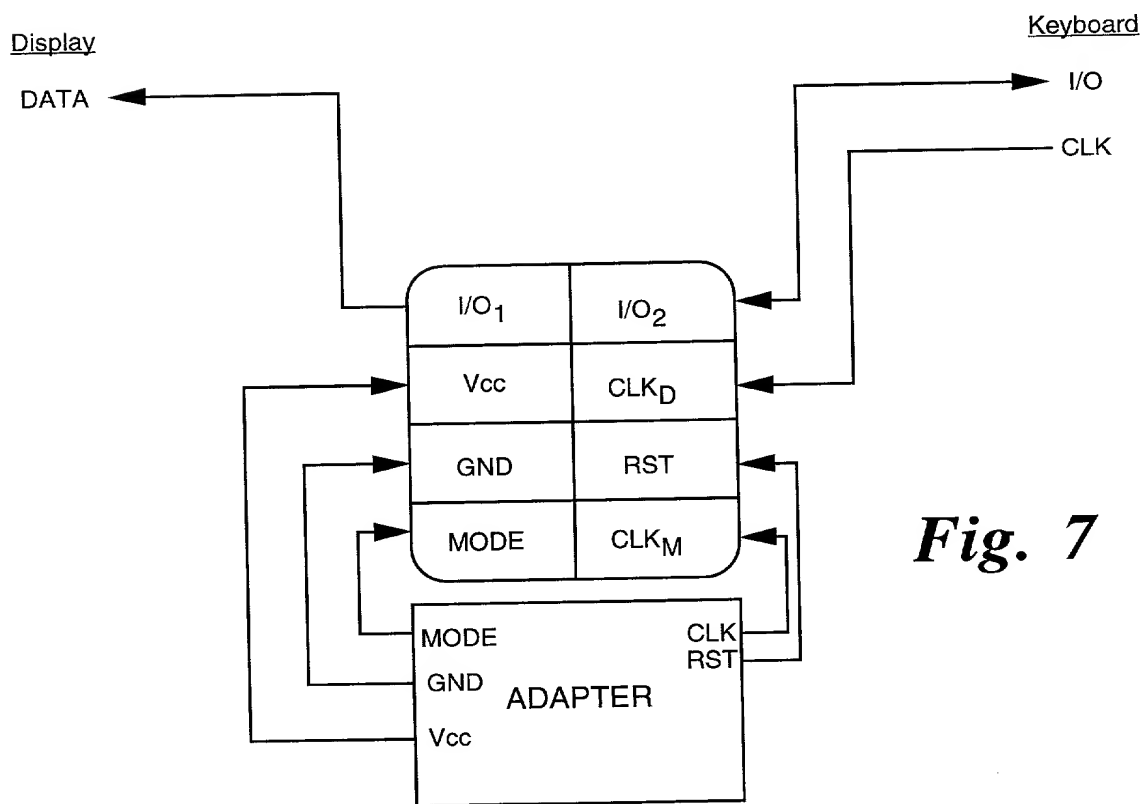
**Fig. 5A**



**Fig. 5B**



**Fig. 6**



**Fig. 7**